

## Assistant COMMISSIONER OF PATENTS AND TRADEMARKS RECEIVED

JUL 0 2 2004

Attorney Docket No.: 95-509

Inventor(s): Erimli et al.

Technology Center 2100

Serial No.: 09/901,683 Filed: July 11, 2001

Title: FAULT TOLERANT COMPUTING NODE HAVING MULTIPLE HOST

**CHANNEL ADAPTERS** 

Date Hand Carried: September 24, 2001

Stamp of the U.S. Patent and Trademark Office acknowledging receipt of the following is requested.

Information Disclosure Statement Form PTO-1449 Document (one copy) Cited in Form PTO-1449 Letter Submitting Formal Drawings four sheets of formal drawings

Papers hand delivered to Assistant Commissioner of Patents and Trademarks

Please return stamped receipt to:

Leon R. Turkevich, Esq. 2000 M Street, N.W., 7<sup>th</sup> Floor Washington, DC 20036-3307

Docket No.:

95-509

## **PATENT**

Group Art Unit: 2183

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

ERIMLI et al.

Serial No.: 09/901,683 :

Filed: July 11, 2001 : Examiner: (Not Assigned)

For: FAULT TOLERANT COMPUTING NODE HAVING MULTIPLE HOST CHANNEL

**ADAPTERS** 

## INFORMATION DISCLOSURE STATEMENT

**RECEIVED** 

JUL 0 2 2004

Assistant Commissioner for Patents
Washington, DC 20231
Technology Center 2100

Sir:

Attached is a Form PTO-1449 (PTO/SB/O8A) listing the enclosed documents. It is respectfully requested that the Examiner consider the enclosed documents in accordance with MPEP 609 by placing the Examiner's initials in the left column of the form PTO-1449, and returning the completed form PTO-1449 to the undersigned upon the next communication from the Patent Office.

This Information Disclosure Statement is being filed within three months of filing the application. No fee or certification is necessary.

This Information Disclosure Statement is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

Respectfully submitted,

Leon R. Turkevich

Registration No. 34,035

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Date: September 24, 2001

PTO/SB/08B (Modified)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (use as many sheets as necessary)				Complete if Known	
				Application Number	09/901,683
				Filing Date	July 11, 2001
				First Named Inventor	Erimli et al.
				Group Art Unit	Not Assigned
				Examiner Name	Not Assigned
Sheet	1	of	1	Attorney Docket Number	95-509

		OTHER PRIOR ART — NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>				
		DANIEL CASSIDAY, InfiniBand™ Architecture Tutorial, Hot Chips, August 2000, Sun Microsystems, 79 pages.			
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			·····		
Examiner Signature		Date Considered			

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>&</sup>lt;sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here is English language Translation is attached.